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U.S. TRADEMARK OFFICE

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent application of  
Yasuhiko TAKEMURA

Serial No. 09/342,235

Filed: June 29, 1999

For: SEMICONDUCTOR DEVICE  
HAVING AT LEAST FIRST AND  
SECOND THIN FILM  
TRANSISTORS

)  
Art Unit: 2826

) Examiner: A. Sefer

CERTIFICATE OF MAILING	
I hereby certify that this correspondence is being deposited with The United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on <u>11/8/02</u>	

*[Signature]*

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
Washington, DC 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith a Form PTO-1449 listing references known to Applicant and requests that these references be considered by the Examiner and made of record in the above identified application. Copies of the references listed are submitted herewith in accordance with 37 C.F.R. 1.98(a).

In accordance with 37 C.F.R. § 1.97(e), it is certified that either (1) each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this information disclosure statement, or (2) no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign patent application and no item of information contained in the information disclosure statement was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this information disclosure statement.

The Commissioner is hereby authorized to charge any fees connected with this filing which may be required now, or credit any overpayment to Deposit Account No. 50-2280.

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Respectfully submitted,

  
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